

**MIDTERM EXAMINATION**  
Spring 2009  
CS302- Digital Logic Design (Session - 2)

**Question No: 1 ( Marks: 1 ) - Please choose one**

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The first Least Significant digit in decimal number system has

- ▶ position 0 and weight equal to 1
- ▶ position 1 and weight equal to 0
- ▶ position 1 and weight equal to 10
- ▶ position 0 and weight equal to 10

**Question No: 2 ( Marks: 1 ) - Please choose one**

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The decimal equivalent of the binary number "10011" is

- ▶ 19
- ▶ 99
- ▶ 29
- ▶ None of given options

**Question No: 3 ( Marks: 1 ) - Please choose one**

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The ANSI/IEEE Standard 754 defines a \_\_\_\_\_ Single-Precision Floating Point format for binary numbers.

- ▶ 8-bit
- ▶ 16-bit
- ▶ 32-bit
- ▶ 64-bit

**Question No: 4 ( Marks: 1 ) - Please choose one**

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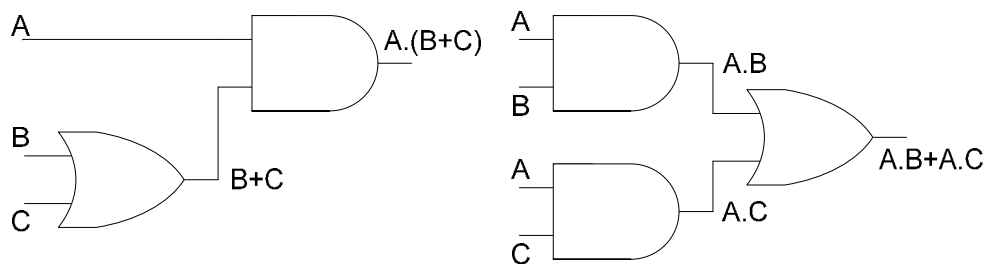
The binary value "11011" is equivalent to \_\_\_\_\_

- ▶ 1B
- ▶ 1C
- ▶ 1D
- ▶ 1E

**Question No: 5 ( Marks: 1 ) - Please choose one**

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The circuit diagram given below explains \_\_\_\_\_

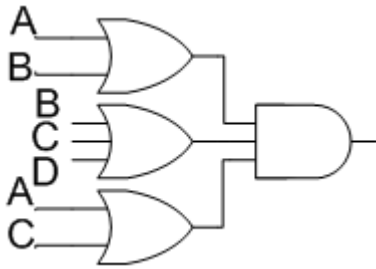


- ▶ Demorgan's Law
- ▶ Commutative Law
- ▶ Associative Law
- ▶ Distributive Law

**Question No: 6 ( Marks: 1 ) - Please choose one**

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The diagram given below represents \_\_\_\_\_



- ▶ Demorgans law
- ▶ Associative law
- ▶ Product of sum form
- ▶ Sum of product form

**Question No: 7 ( Marks: 1 ) - Please choose one**

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NOR gate is formed by connecting \_\_\_\_\_

- ▶ OR Gate and then NOT Gate
- ▶ NOT Gate and then OR Gate
- ▶ AND Gate and then OR Gate
- ▶ OR Gate and then AND Gate

**Question No: 8 ( Marks: 1 ) - Please choose one**

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“74ALS” stands for \_\_\_\_\_

- ▶ Advanced Low-frequency Schottky TTL
- ▶ Advanced Low-dissipation Schottky TTL
- ▶ Advanced Low-Power Schottky TTL
- ▶ Advanced Low-propagation Schottky TTL